



7th - 11th JANUARY, 2012

Hyderabad International Convention Centre (HICC), Hyderabad - INDIA

www.vlsiconference.com



VLSID 2012 – CALL FOR PARTICIPATION

This **Joint Silver Jubilee International Conference on VLSI Design and 11th International Conference on Embedded Systems (VLSID 2012)** is a forum for researchers and designers to present and discuss various aspects of VLSI Design, EDA, Embedded Systems, and Enabling Technologies. The conference is a platform for technical exchanges by experts from all over the world. It is a great opportunity for companies to showcase their products and solutions, network, to expand businesses and to increase visibility through sponsorship.

This **five-day** conference will comprise of **two 'Full Day Tutorial Sessions'**, followed by three days of Keynote Talks, Regular Paper Sessions, Special Sessions, and Embedded Tutorials.

We are proud to invite you to the **Pearl City of India, Hyderabad**, which is a perfect setting for VLSID 2012. This wonderful city is one of the largest and technologically advanced cities in Southern India. Highly motivated organizing committee members of VLSID 2012 are dedicated to making this conference in Hyderabad both unforgettable and scientifically exceptional.

We are all looking forward to seeing you in Hyderabad, India.

Theme:

Embedded Solutions for Emerging Markets – Consumer, Energy, Automotive

Topics

VLSI Design and Methodologies

- Analog and RF Mixed Signal Design
- ASIC Physical Design, Technology CAD
- Synthesis, DFT, DFM, Test and Verification
- Power Analysis and Low-Power Design
- High Performance Computing
- Networks-on-Chip
- Emerging Technologies: CMOS sensors, MEMS, Nanoscale computing

Embedded Systems

- Audio, Image and Video Processing
- Hardware/Software Co-design and Verification
- Reconfigurable Systems, Hybrid Systems-on-Chip
- Applications of Embedded Systems

Full Day Tutorials

January 7, 2012

- Design of Mixed-Signal Systems using SystemC AMS extensions (T1)
- Reversible Logic: Fundamentals and Applications in Emerging Nanotechnologies (T2)
- DFM, DFT, Silicon Debug and Diagnosis – The loop to ensure product yield (T3)
- Intellectual Property Protection and Security in SoC (T4)

January 8, 2012

- Advanced Analog-Mixed Signal System and Circuit Techniques (T5)
- Variability-resistant SW / HW for Nano-Scale Computing (T6)
- New Modeling Methodologies for Thermal Analysis of 3D ICs and Advanced Cooling Technologies of the Future (T7A)
- Optimally Addressing Verification Constraint Complexity for Effective Functional Convergence (T7B)
- Designing Silicon-Photonic Communication Networks for Many Core Systems (T8A)
- Wireless System Design and Systems Engineering Challenges (T8B)

Conference Highlights

- Keynote Talks from Industry Visionaries
- 2 Full-day Tutorials by Academia & Industry Experts
- 3-day Strong Technical Program comprising of Regular Paper Sessions, Special Sessions, and Embedded Tutorials
- Exhibits displaying Latest Products & Solutions
- Fellowship Program to support Students, & Faculty
- Design Contest
- **Student Project Contest (For the first time)**
- Students Forum
- Industry Forum & RASDAT 2012 Workshop (Jan 7-8)
- Banquet Dinners
- Grand Silver Jubilee Celebrations & Cultural Programs

Conference Registration Fee Details

	Members (IEEE/VSII/ACM)		Non Members	
	Upto Nov 30th 2011	From Dec 1st 2011	Upto Nov 30th 2011	From Dec 1st 2011
Conference (3 Days)	INR 6800 / USD 200	INR 8000 / USD 230	INR 8400 / USD 250	INR 9500 / USD 280
Tutorials 1 Day	INR 2600 / USD 80	INR 3200 / USD 90	INR 3200 / USD 90	INR 3700 / USD 110
Tutorials 2 Days	INR 4700 / USD 140	INR 5800 / USD 170	INR 5800 / USD 170	INR 6800 / USD 200

Students Forum Registration Fee Details

	INR	USD	INR	USD
Students Forum Registration Fee Before 01-12-11	500	15 (1 Day)	900	25 (2 Days)
Students Forum Registration Fee After 01-12-11	700	20 (1 Day)	1100	30 (2 Days)

For Registrations, Sponsorship Opportunities
and for Exhibit Bookings, please visit

www.vlsiconference.com

For further details please contact

VLSI Secretariat

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